

## DSS34 SCHOTTKY BARRIER RECTIFIER

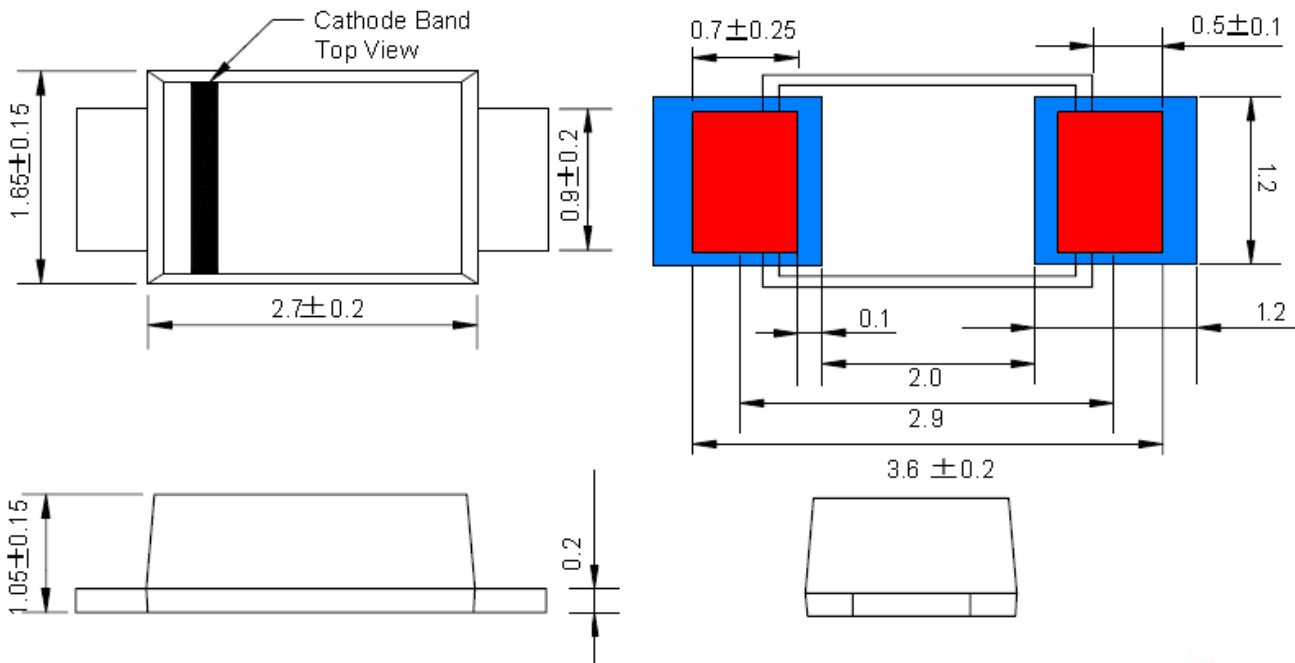
### Features

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Metal silicon junction, majority carrier conduction
- For surface mount applications
- Low power loss, high efficiency
- High current capability, Low forward voltage drop
- Low profile package
- Built-in strain relief, ideal for automated placement
- For use in low voltage, high frequency inverters, free wheeling, and polarity applications
- High temperature soldering guaranteed: 260°C/10 seconds at terminals

### Mechanical Data

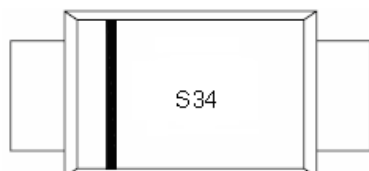
- Case: JEDEC SOD-123FL molded plastic body
- Terminals: Solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Weight: 0.0007 ounce, 0.02 grams

### Mechanical Dimensions (In mm)



Note: Blue area is suggested pad layout and red area is package terminals.

### SOD-123FL

**Marking Diagram:**


S34 = Marking code

**Cautions** : Molding resin  
 Epoxy resin UL:94V-0

**Ordering Information:**

Device	Package	Shipping
DSS34	SOD-123FL	3000pcs / reel

For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification

**Maximum Ratings and Electrical characteristics**

Ratings at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20 %

Characteristic	Symbol	DSS34	Unit
Marking code		S34	
Maximum Repetitive Peak Reverse Voltage Maximum DC Blocking Voltage	$V_{RRM}$ $V_{DC}$	40	V
Maximum RMS voltage	$V_{RMS}$	28	V
Maximum Average Forward Rectified Current (See fig.1)	$I_{F(AV)}$	3.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	80.0	A
Max Instantaneous Forward Voltage at 3.0A (Note 1)	$V_F$	0.50	V
Peak Reverse Current (Note 1) @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage (Note 1) @ $T_A = 100^\circ\text{C}$	$I_{RM}$	0.2 20	mA
Typical Junction Capacitance(Note 3)	$C_J$	250	pF
Typical Thermal Resistance(Note 2)	$R_{\theta JA}$ $R_{\theta JL}$	55 17	$^\circ\text{C/W}$
Operating Temperature Range	$T_J$	-65 to +150	$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-65 to +150	$^\circ\text{C}$

Note: 1. Pulse test: 300 us pulse width, 1% duty cycle.

2. PCB mounted on 0.55 X 0.55" (14 X 14 mm) copper pad areas.

3. Measured at 1MHz and applied reverse voltage of 4V D.C





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